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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/800,012	03/05/2001	Jose L. Cervantes	10007303-1	5210

7590 10/29/2003
HEWLETT-PACKARD COMPANY
Intellectual Property Administration
P.O. Box 272400
Fort Collins, CO 80527-2400

EXAMINER

VO, TIM T

ART UNIT	PAPER NUMBER
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2189

DATE MAILED: 10/29/2003

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Please find below and/or attached an Office communication concerning this application or proceeding.

PRG

Office Action Summary

Application No.

09/800,012

Applicant(s)

CERVANTES, JOSE L.

Examiner

Tim T. Vo

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 05 March 2001.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-35 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-35 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 05 March 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892) 4) ☐ Interview Summary (PTO-413) Paper No(s). _____
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948) 5) ☐ Notice of Informal Patent Application (PTO-152)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____ 6) ☐ Other:

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Part III DETAILED ACTION

Notice to Applicant(s)

This application has been examined. Claims 1-35 are pending.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. § 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1-35 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Lin patent number 5,717,957 referred hereinafter "Lin" in view of Rathonyi et al. patent number 6,359,877 referred hereinafter "Rathonyi".

As for claims 1, 11, 18, 25 and 30-32, Lin teaches a multifunctional peripheral device comprising:

a printer system (see figure 1, P1-P2 ports and column 2 lines 49-65, wherein printers connecting to the P1 and P2 ports);

a scanner system (see figure 1, P3 port and column 2 lines 49-65, wherein scanner connecting to the P3 port);

an input/output (I/O) system (see figures 1-2 and column 2 lines 57-60, wherein the I/O system is the I/O card within the personal computer system);

a processor system (see figures 1-2, decoder 10 and column 2 lines 66 to column 3 line 24, wherein the decoder receives instruction signals and process them by

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identifying the input instruction signals and , if it is matched with the preset control instruction, the decoder circuit 10 transmits a proper control instruction to the function selection circuit 20);

a switch fabric for routing data between the printer system, the scanner system, the IO system, and the processor system through switch IO buses, the switch IO buses providing a point to point dedicated interconnection between the switch fabric and each of the printer system, the scanner system, the IO system and the processor system (see figure 1, switch 30 and column 2 line 65 to column 4 line 33, wherein the I/O card connecting to the port1 or LPT1-2 in order to transfer control instruction signals to the decoder 10 for processing. Further, the decoder 10 processed the instruction signals and transmits control signal to the selection circuit 20 for selecting output from 1A and 1B of the decoder 10 and last the switch 30 is a point to point interconnection for switching connection to the printer, scanner, I/O card, decoder 10).

Lin does not expressly teach wherein data transferring between the I/O card, printer, scanner, decoder 10, selection circuit 20 and switch 30 in packet based. Column 1 lines 23-26, Lin suggested several ways to improve computer environment, such as data transmission, demand quantity, data processing speed, and other properties are important to consumers but Lin fails to disclose how to improve such improvements. However, Rathonyi teaches data transmission in packet based (see column 2 lines 17-25. Therefore, it would have been obvious to a person of an ordinary skill in the art at the time the invention was made to have combined the teachings of Rathonyi into the

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teachings of Lin's because the teaching of Rathonyi in data transmission in packet base would increase transmission speed, which Lin fails to disclose.

As for claims 2, 12, 19, 26 and 33, Lin teaches wherein each system is integrated onto a distinct application specific integrated circuit (ASIC) (see figure 1, wherein each system is separated from each other).

As for claims 3, 13, 20, 27 and 34, Lin teaches a switch IO bus providing point to point dedicated interconnection between the printer system and the scanner system which permits the transfer data directly between the printer system and the scanner (see figure 1 and column 6 line 48 to column 8 line 9).

As for claims 4, 14, 21, 28 and 35, Lin teaches a switch IO bus providing point to point dedicated interconnection between the printer system and the IO system which permits the transfer of data directly between the printer system and the IO system (see figure 1 and column 6 line 48 to column 8 line 9).

As for claims 5, 15 and 22, Lin teaches a switch IO bus providing a point to point dedicated interconnection between the printer system and the processor system which permits the transfer of data directly between the printer system and the processor system (see figure 1 and column 6 line 48 to column 8 line 9).

As for claim 6, Lin teaches a switch IO bus providing a point to point dedicated interconnection between the scanner system and the IO system which permits the transfer of data directly between the scanner and the IO system (see figure 1 and column 6 line 48 to column 8 line 9).

As for claims 7 and 24, Lin teaches a switch IO bus providing a point to point dedicated interconnection between the scanner system and the processor system which permits the transfer of data directly between the scanner and the processor system (see figure 1 and column 6 line 48 to column 8 line 9).

As for claim 8, Lin teaches a switch IO bus providing point to point dedicated interconnection between the processor system and the IO system which permits the transfer of data directly between the processor system and the IO system (see figure 1 and column 6 line 48 to column 8 line 9).

As for claims 9, 16, 23 and 29, Lin fails to teach PCI bridge connecting to IO switch. However, Lin suggested other modifications variations and changes will be suggested to persons skilled in the art would fall within the scope of his invention (see column 6 lines 39-46). Therefore, it would have been obvious to a person of an ordinary skill in the art at the time the invention was made to use a PCI bridge coupling to the switching 30 of Lin would provide system expansion.

As for claims 10 and 17, Lin teaches one or more IO ports (see figure 1);
a second switch fabric for routing data between the one or more enhanced IO ports and the switch fabric through switch IO buses, the switch IO buses providing point to point dedicated interconnections between each of the one or more enhanced IO ports and the second switch fabric, and between the second switch fabric and the switch fabric (see column 3 lines 58-67, wherein there are at least 3 multiplex circuits U12-14 for routing data).

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tim T. Vo whose telephone number is 703-308-5862.

The examiner can normally be reached on 7:30-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mark Rinehart can be reached on 703-305-4815. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-2100.

A handwritten signature in black ink, appearing to read 'Tim Vo', with a stylized flourish at the end.

Tim T. Vo
Examiner
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T.V
10/23/03